



**SINGLE-CHANNEL**  
**6N135, 6N136**  
**HCPL-2503**  
**HCPL-4502**

**DUAL-CHANNEL**  
**HCPL-2530**  
**HCPL-2531**

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 0$ to $70^\circ\text{C}$ Unless otherwise specified)							
<b>INDIVIDUAL COMPONENT CHARACTERISTICS</b>							
Parameter	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit
<b>EMITTER</b>							
Input Forward Voltage	( $I_F = 16$ mA, $T_A = 25^\circ\text{C}$ )	$V_F$			1.45	1.7	V
	( $I_F = 16$ mA)					1.8	
Input Reverse Breakdown Voltage	( $I_R = 10$ $\mu\text{A}$ )	$B_{VR}$		5.0			V
Temperature coefficient of forward voltage	( $I_F = 16$ mA)	( $\Delta V_F / \Delta T_A$ )			-1.6		mV/ $^\circ\text{C}$
<b>DETECTOR</b>							
Logic high output current	( $I_F = 0$ mA, $V_O = V_{CC} = 5.5$ V) ( $T_A = 25^\circ\text{C}$ )	$I_{OH}$	All		0.001	0.5	$\mu\text{A}$
	( $I_F = 0$ mA, $V_O = V_{CC} = 15$ V) ( $T_A = 25^\circ\text{C}$ )		6N135 6N136 HCPL-4502 HCPL-2503		0.005	1	
	( $I_F = 0$ mA, $V_O = V_{CC} = 15$ V)		All			50	
Logic low supply current	( $I_F = 16$ mA, $V_O = \text{Open}$ ) ( $V_{CC} = 15$ V)	$I_{CCL}$	6N135 6N136 HCPL-4502 HCPL-2503		120	200	$\mu\text{A}$
	( $I_{F1} = I_{F2} = 16$ mA, $V_O = \text{Open}$ ) ( $V_{CC} = 15$ V)		HCPL-2530 HCPL-2531		200	400	
Logic high supply current	( $I_F = 0$ mA, $V_O = \text{Open}$ , $V_{CC} = 15$ V) ( $T_A = 25^\circ\text{C}$ )	$I_{CCH}$	6N135 6N136 HCPL-4502 HCPL-2503			1	$\mu\text{A}$
	( $I_F = 0$ mA, $V_O = \text{Open}$ ) ( $V_{CC} = 15$ V)		6N135 6N136 HCPL-4502 HCPL-2503			2	
	( $I_F = 0$ mA, $V_O = \text{Open}$ ) ( $V_{CC} = 15$ V)		HCPL-2530 HCPL-2531		0.02	4	

\*\* All typicals at  $T_A = 25^\circ\text{C}$

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<b>TRANSFER CHARACTERISTICS</b> ( $T_A = 0$ to $70^\circ\text{C}$ Unless otherwise specified)									
Parameter	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit		
<b>COUPLED</b>  Current transfer ratio (Note 5)	$(I_F = 16 \text{ mA}, V_O = 0.4 \text{ V})$ $(V_{CC} = 4.5 \text{ V}, T_A = 25^\circ\text{C})$	CTR	6N135 HCPL-2530	7	18	50	%		
			6N136 HCPL-4502 HCPL-2531	19	27	50	%		
			HCPL-2503	12	27		%		
			6N135 HCPL-2530	5	21		%		
	$(I_F = 16 \text{ mA}, V_O = 0.5 \text{ V})$ $(V_{CC} = 4.5 \text{ V})$		6N136 HCPL-4502 HCPL-2531	15	30		%		
			HCPL-2503	9	30		%		
			Logic low output voltage output voltage	$(I_F = 16 \text{ mA}, I_O = 1.1 \text{ mA})$ $(V_{CC} = 4.5 \text{ V}, T_A = 25^\circ\text{C})$	6N135 HCPL-2530		0.18	0.4	V
					6N136 HCPL-4502 HCPL-2503		0.18	0.5	
$(I_F = 16 \text{ mA}, I_O = 3 \text{ mA})$ $(V_{CC} = 4.5 \text{ V}, T_A = 25^\circ\text{C})$	HCPL-2531			0.25	0.4				
$(I_F = 16 \text{ mA}, I_O = 0.8 \text{ mA})$ $(V_{CC} = 4.5 \text{ V})$	6N135 HCPL-2530				0.5				
$(I_F = 16 \text{ mA}, I_O = 2.4 \text{ mA})$ $(V_{CC} = 4.5 \text{ V})$	6N136 HCPL-4502 HCPL-2503 HCPL-2531				0.5				

\*\* All typicals at  $T_A = 25^\circ\text{C}$

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## SWITCHING CHARACTERISTICS ( $T_A = 0$ to $70^\circ\text{C}$ unless otherwise specified., $V_{CC} = 5\text{ V}$ )

Parameter	Test Conditions	Symbol	Device	Min	Typ**	Max	Unit
Propagation delay time to logic low	$T_A = 25^\circ\text{C}$ , ( $R_L = 4.1\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 6) (Fig. 7)	$T_{PHL}$	6N135 HCPL-2530		0.45	1.5	$\mu\text{s}$
	$(R_L = 1.9\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 7) (Fig. 7) $T_A = 25^\circ\text{C}$		6N136 HCPL-4502 HCPL-2503 HCPL-2531		0.45	0.8	$\mu\text{s}$
	$(R_L = 4.1\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 6) (Fig. 7)		6N135 HCPL-2530			2.0	$\mu\text{s}$
	$(R_L = 1.9\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 7) (Fig. 7)		6N136 HCPL-4502 HCPL-2503 HCPL-2531			1.0	$\mu\text{s}$
Propagation delay time to logic high	$T_A = 25^\circ\text{C}$ , ( $R_L = 4.1\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 6) (Fig. 7)	$T_{PLH}$	6N135 HCPL-2530		0.5	1.5	$\mu\text{s}$
	$(R_L = 1.9\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 7) (Fig. 7) $T_A = 25^\circ\text{C}$		6N136 HCPL-4502 HCPL-2503 HCPL-2531		0.3	0.8	$\mu\text{s}$
	$(R_L = 4.1\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 6) (Fig. 7)		6N135 HCPL-2530			2.0	$\mu\text{s}$
	$(R_L = 1.9\text{ k}\Omega$ , $I_F = 16\text{ mA}$ ) (Note 7) (Fig. 7)		6N136 HCPL-4502 HCPL-2503 HCPL-2531			1.0	$\mu\text{s}$
Common mode transient immunity at logic high	$(I_F = 0\text{ mA}$ , $V_{CM} = 10\text{ V}_{P-P}$ , $R_L = 4.1\text{ k}\Omega$ ) (Note 8) (Fig. 8) $T_A = 25^\circ\text{C}$	$ CM_H $	6N135 HCPL-2530		10,000		$\text{V}/\mu\text{s}$
	$(I_F = 0\text{ mA}$ , $V_{CM} = 10\text{ V}_{P-P}$ ) $T_A = 25^\circ\text{C}$ , ( $R_L = 1.9\text{ k}\Omega$ ) (Note 8) (Fig. 8)		6N136 HCPL-4502 HCPL-2503 HCPL-2531		10,000		$\text{V}/\mu\text{s}$
Common mode transient immunity at logic low	$(I_F = 16\text{ mA}$ , $V_{CM} = 10\text{ V}_{P-P}$ , $R_L = 4.1\text{ k}\Omega$ ) (Note 8) (Fig. 8) $T_A = 25^\circ\text{C}$	$ CM_L $	6N135 HCPL-2530		10,000		$\text{V}/\mu\text{s}$
	$(I_F = 16\text{ mA}$ , $V_{CM} = 10\text{ V}_{P-P}$ ) $(R_L = 1.9\text{ k}\Omega)$ (Note 8) (Fig. 8)		6N136 HCPL-4502 HCPL-2503 HCPL-2531		10,000		$\text{V}/\mu\text{s}$

\*\* All typicals at  $T_A = 25^\circ\text{C}$

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<b>ISOLATION CHARACTERISTICS</b> ( $T_A = 0$ to $70^\circ\text{C}$ Unless otherwise specified)						
Characteristics	Test Conditions	Symbol	Min	Typ**	Max	Unit
Input-output insulation leakage current	(Relative humidity = 45%) ( $T_A = 25^\circ\text{C}$ , $t = 5$ s) ( $V_{I-O} = 3000$ VDC) (Note 9)	$I_{I-O}$			1.0	$\mu\text{A}$
Withstand insulation test voltage	( $RH \leq 50\%$ , $T_A = 25^\circ\text{C}$ ) (Note 9) ( $t = 1$ min.)	$V_{ISO}$	2500			$V_{RMS}$
Resistance (input to output)	(Note 9) ( $V_{I-O} = 500$ VDC)	$R_{I-O}$		$10^{12}$		$\Omega$
Capacitance (input to output)	(Note 9) ( $f = 1$ MHz)	$C_{I-O}$		0.6		pF
DC Current gain	( $I_O = 3$ mA, $V_O = 5$ V)	HFE		150		
Input-Input Insulation leakage current	( $RH \leq 45\%$ , $V_{I-I} = 500$ VDC) (Note 10) $t = 5$ s, (HCPL-2530/2531 only)	$I_{I-I}$		0.005		$\mu\text{A}$
Input-Input Resistance	( $V_{I-I} = 500$ VDC) (Note 10) (HCPL-2530/2531 only)	$R_{I-I}$		$10^{11}$		$\Omega$
Input-Input Capacitance	( $f = 1$ MHz) (Note 10) (HCPL-2530/2531 only)	$C_{I-I}$		0.03		pF

\*\* All typicals at  $T_A = 25^\circ\text{C}$

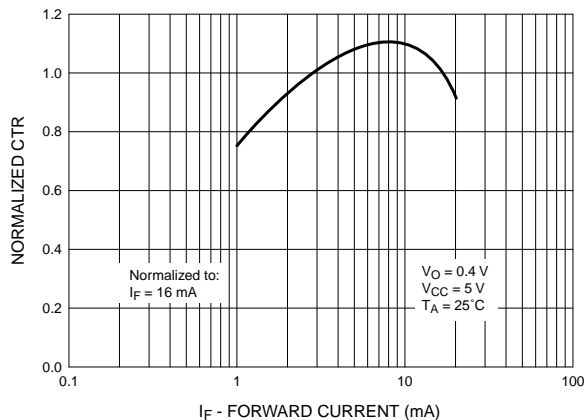
## NOTES

1. Derate linearly above  $70^\circ\text{C}$  free-air temperature at a rate of  $0.8$  mA/ $^\circ\text{C}$ .
2. Derate linearly above  $70^\circ\text{C}$  free-air temperature at a rate of  $1.6$  mA/ $^\circ\text{C}$ .
3. Derate linearly above  $70^\circ\text{C}$  free-air temperature at a rate of  $0.9$  mW/ $^\circ\text{C}$ .
4. Derate linearly above  $70^\circ\text{C}$  free-air temperature at a rate of  $2.0$  mW/ $^\circ\text{C}$ .
5. Current Transfer Ratio is defined as a ratio of output collector current,  $I_O$ , to the forward LED input current,  $I_F$ , times 100%.
6. The  $4.1$  k $\Omega$  load represents 1 LSTTL unit load of  $0.36$  mA and  $6.1$  k $\Omega$  pull-up resistor.
7. The  $1.9$  k $\Omega$  load represents 1 TTL unit load of  $1.6$  mA and  $5.6$  k $\Omega$  pull-up resistor.
8. Common mode transient immunity in logic high level is the maximum tolerable (positive)  $dV_{cm}/dt$  on the leading edge of the common mode pulse signal  $V_{CM}$ , to assure that the output will remain in a logic high state (i.e.,  $V_O > 2.0$  V). Common mode transient immunity in logic low level is the maximum tolerable (negative)  $dV_{cm}/dt$  on the trailing edge of the common mode pulse signal,  $V_{CM}$ , to assure that the output will remain in a logic low state (i.e.,  $V_O < 0.8$  V).
9. Device is considered a two terminal device: Pins 1, 2, 3 and 4 are shorted together and Pins 5, 6, 7 and 8 are shorted together.
10. Measured between pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

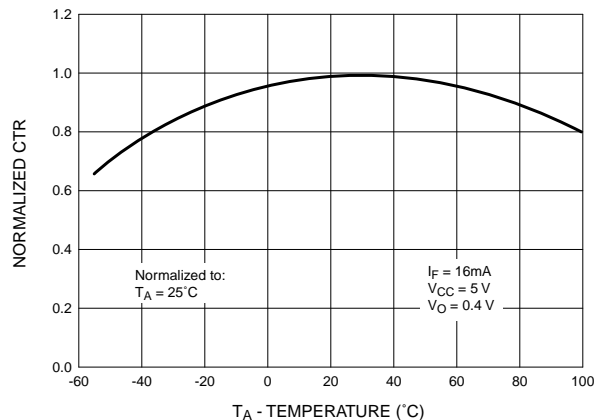
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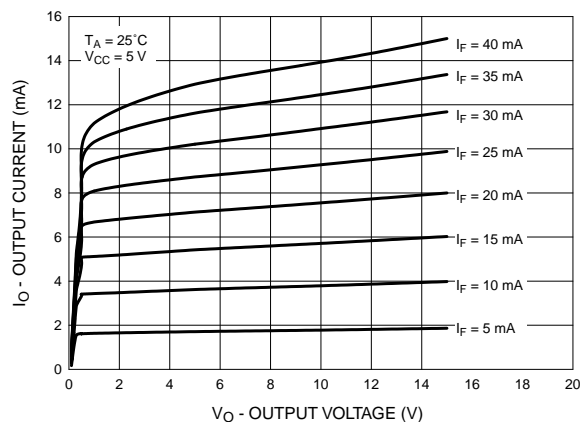
**Fig. 1 Normalized CTR vs. Forward Current**



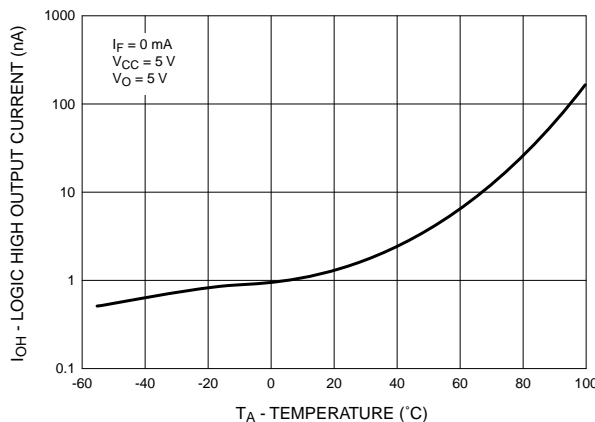
**Fig. 2 Normalized CTR vs. Temperature**



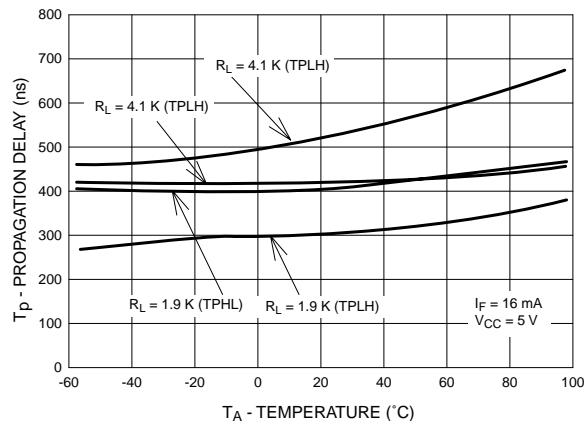
**Fig. 3 Output Current vs. Output Voltage**



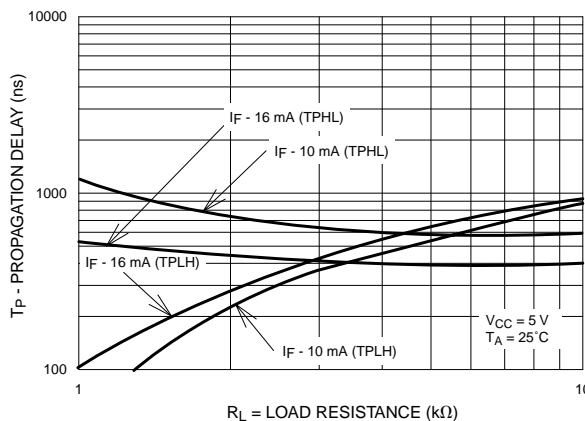
**Fig. 4 Logic High Output Current vs. Temperature**



**Fig. 5 Propagation Delay vs. Temperature**

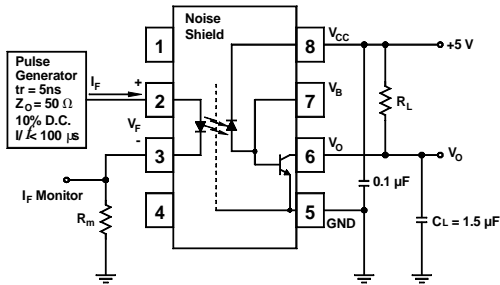


**Fig. 6 Propagation Delay vs. Load Resistance**

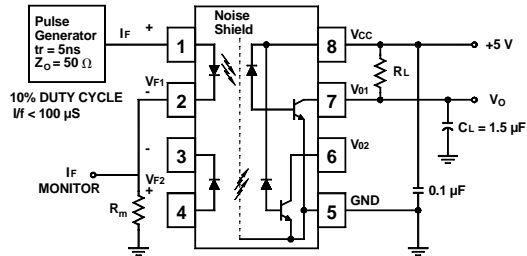


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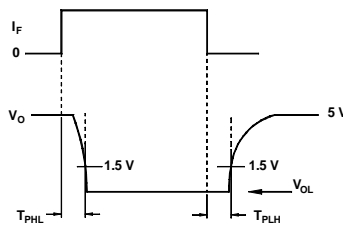
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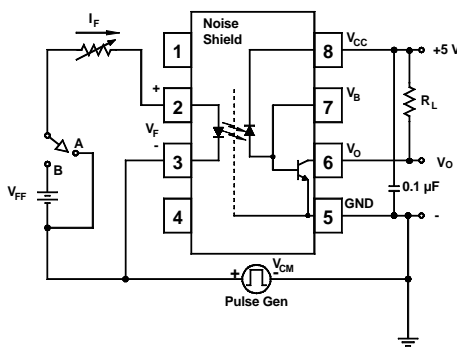
Test Circuit for 6N135, 6N136, HCPL-2503 and HCPL-4502



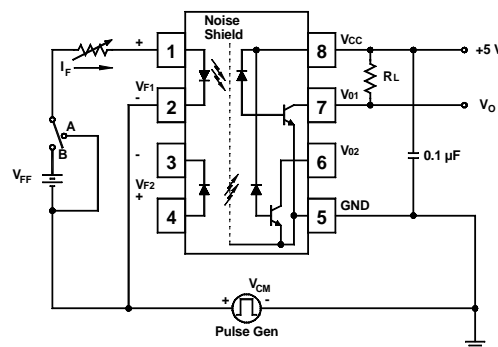
Test Circuit for HCPL-2530 and HCPL-2531



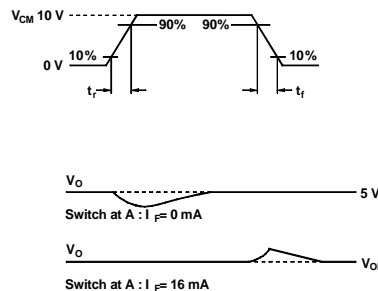
**Fig. 7 Switching Time Test Circuit**



Test Circuit for 6N135, 6N136, HCPL-2503 and HCPL-4502



Test Circuit for HCPL-2530 and HCPL-2531

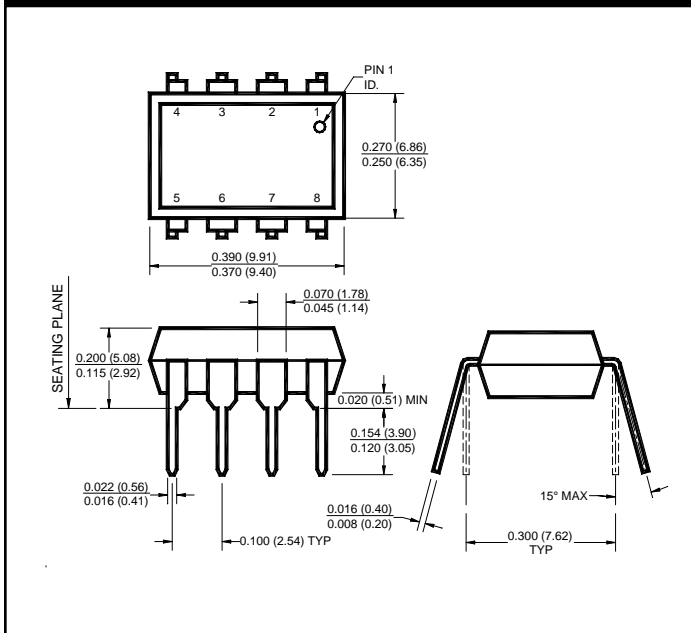


**Fig. 8 Common Mode Immunity Test Circuit**

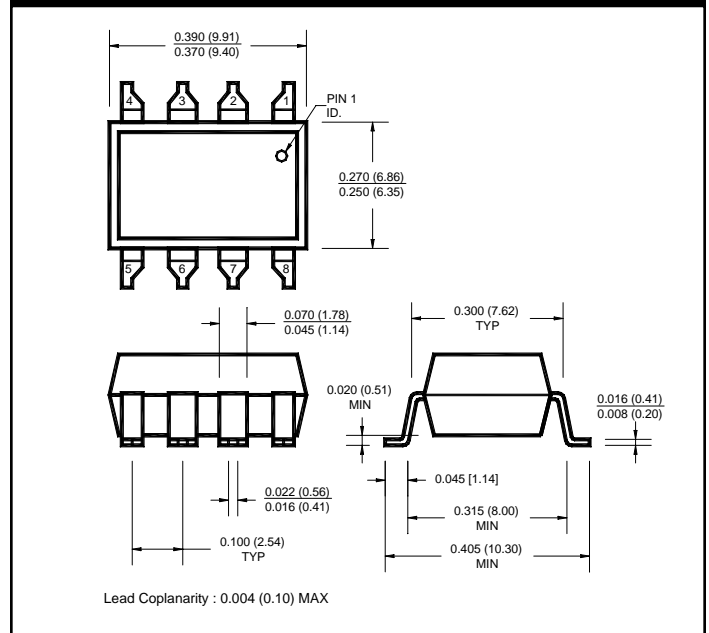
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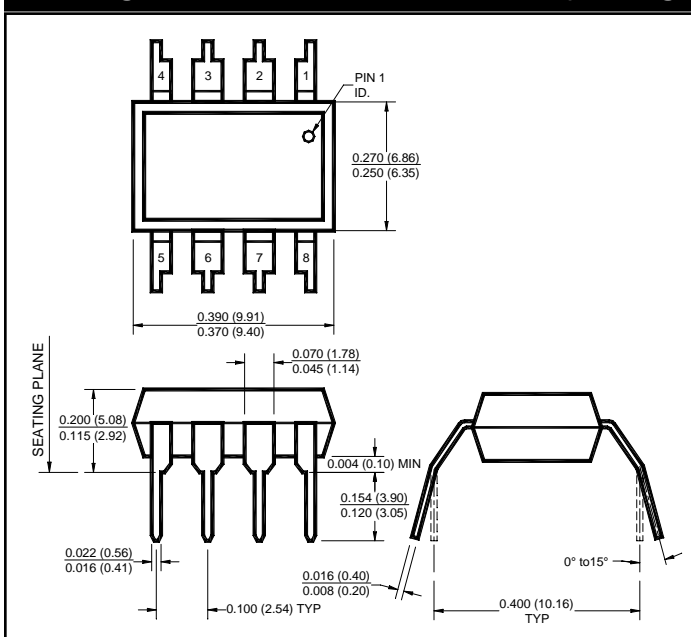
## Package Dimensions (Through Hole)



## Package Dimensions (Surface Mount)



## Package Dimensions (0.4" Lead Spacing)



### NOTE

All dimensions are in inches (millimeters)

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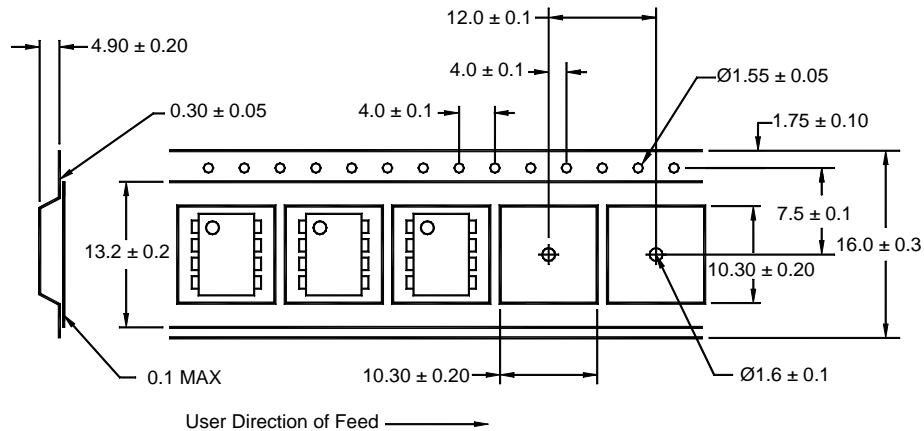
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## ORDERING INFORMATION

Option	Order Entry Identifier	Description
R2	.R2	Opto Plus Reliability Conditioning
S	.S	Surface Mount Lead Bend
SD	.SD	Surface Mount; Tape and reel
SDL	.SDL	Surface Mount; Tape and reel
W	.W	0.4" Lead Spacing

## QT Carrier Tape Specifications ("D" Taping Orientation)



### Corporate Headquarters

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